

1 1. A method comprising:
 2 resolving a display into at least two regions;
 3 generating a different sequence of characteristic
 4 values in each region; and
 5 resolving the position of a sensor with respect
 6 to said regions.

1 2. The method of claim 1 wherein resolving a display
 2 into at two regions includes resolving a display into at
 3 least four regions.

1 3. The method of claim 1 wherein generating a
 2 different sequence includes generating a different sequence
 3 of color values in each region.

1 4. The method of claim 3 including generating a
 2 different sequence of at least three color values.

1 5. The method of claim 3 including generating a
 2 different sequence of only two color values.

1 6. The method of claim 1 including displaying a
 2 series of frames and interspersing, among said frames,
 3 additional frames having at least two regions each
 4 displaying a sequence of characteristic values.

1 7. The method of claim 6 including displaying said
2 additional frames in a fashion such that they are
3 substantially undetectable by the user.

1 8. The method of claim 1 including generating a
2 different sequence of characteristic values by displaying a
3 time sequence of frames each including at least two
4 regions, and each of said regions displaying a timed
5 sequence of characteristic values.

1 9. The method of claim 8 including interspersing
2 frames containing said characteristic values and frames not
3 containing said characteristic values.

1 10. The method of claim 1 including developing a
2 sequence using fewer characteristic values than the number
3 of regions.

1 11. An article comprising a medium storing
2 instructions that enable a processor-based system to:
3 resolve a display into at least two regions; and
4 generate a different sequence of characteristic
5 values in each region.

1 12. The article of claim 11 further storing
2 instructions that enable the processor-based system to

3 resolve the position of a sensor with respect to said
4 regions.

1 13. The article of claim 11 further storing
2 instructions that enable the processor-based system to
3 resolve the display into at least four regions.

1 14. The article of claim 11 further storing
2 instructions that enable the processor-based system to
3 generate a different sequence of color values in each
4 region.

1 15. The article of claim 14 further storing
2 instructions that enable the processor-based system to
3 generate a different sequence of at least three color
4 values in each region.

1 16. The article of claim 14 further storing
2 instructions that enable the processor-based system to
3 generate a different sequence of only two color values in
4 each region.

1 17. The article of claim 11 further storing
2 instructions that enable the processor-based system to
3 cause a series of frames to be displayed while
4 interspersing, among said frames, additional frames having

5 at least two regions each displaying a sequence of
6 characteristic values.

1 18. The article of claim 11 further storing
2 instructions that enable the processor-based system to
3 generate a different sequence of characteristic values by
4 displaying a time sequence of frames each including at
5 least two regions, and each of said regions displaying a
6 time sequence of characteristic values.

1 19. The article of claim 18 further storing
2 instructions that enable the processor-based system to
3 intersperse frames containing said characteristic values
4 and frames not containing said characteristic values.

1 20. A system comprising:
2 a processor;
3 a memory coupled to said processor, said memory
4 storing instructions that enable the system to resolve a
5 display into at least two regions and generate a different
6 sequence of characteristic values in each region.

1 21. The system of claim 20 including a display
2 coupled to said processor.

1 28. The system of claim 20 wherein said storage
2 stores instructions that enable the system to generate a
3 different sequence of characteristic values by displaying a
4 time sequence of frames each including at least two
5 regions, and each of said regions displaying a time
6 sequence of characteristic values.

1 29. The system of claim 20 including a sensor coupled
2 to said processor.

1 30. The system of claim 29 wherein said sensor is a
2 light sensor that detects a characteristic value in the
3 form of light.